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2183  
August 26, 2004

Applicants: Gyle D. Yearsley et al.

Title: Context Switching Pipelined Microprocessor

Application No.: 09/909,507 Filing Date: July 20, 2001

Examiner: Pan, Daniel H. Group Art Unit: 2183

Docket No.: ZILG.252US0 Conf. No.: 4144

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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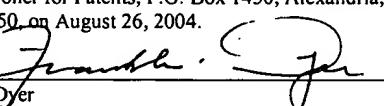
Transmitted herewith are the following documents in the above-identified application:

- (1) Return Receipt Postcard;
- (2) This Transmittal Letter (1 page)
- (3) Amendment (10 pages);
- (4) Supplemental Information Disclosure Statement (2 pages);
- (5) PTO Form 1449 (1 page);
- (6) 2 Cited References; and
- (7) Check for \$180.00.

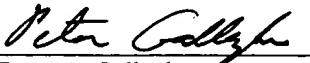
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Respectfully submitted,

  
Peter A. Gallagher 8/26/04  
Reg. No.47,584 Date